

All-Optical Clock Recovery for NRZ-DPSK Signals

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Abstract—We experimentally demonstrate an optical clock recovery scheme for nonreturn-to-zero differential phase shifting keying (NRZ-DPSK) data. By using an optical circuit made by a proper fiber Bragg filter and a Fabry-Pérot based clock extraction circuit, we obtain a stable and low jitter 10-GHz optical clock signal. This signal shows comparable performance with the original electrical clock in bit-error-rate measurements and oscilloscope triggering operation.

Index Terms—All-optical clock recovery, Fabry-Pérot filter, optical communications, optical DPSK, semiconductor optical amplifier (SOA).

I. INTRODUCTION

THE synchronization issue is a serious challenge in several synchronous and asynchronous network architectures under study. In future high-speed transparent networks, a reliable optical synchronization signal could be needed for several applications: namely, in receivers, in 3R regenerators and, in general, in all optical signal processing [1]. On the other hand, phase modulated signals, as differential phase shifted keying (DPSK) modulation formats, acquire increasing popularity because of a number of advantages in respect of the usual ON-OFF keyed (OOK) format. They show high resilience to nonlinear impairments [2] and, when using balanced detection, have around 3-dB optical signal-to-noise ratio (OSNR) advantage in respect of OOK. Regarding DPSK, while return-to-zero DPSK (RZ-DPSK) signals show strong clock components in both the optical and electrical spectrum, nonreturn-to-zero DPSK (NRZ-DPSK) signals have ideally no clock lines and, in practical realizations, only weak clock components. This makes by far more difficult the realization of clock recovery systems for NRZ-DPSK data. In principle, it could be possible to use established clock recovery schemes after DPSK demodulation and a following preprocessing stage used to emphasize the weak clock lines. Anyway, a simpler technique able to directly extract the clock would be preferred. Here, we experimentally demonstrate, for the first time to our knowledge, an optical circuit capable of NRZ-DPSK clock recovery. This circuit is mainly based on proper selective optical filtering and uses a semiconductor optical amplifier (SOA) for signal equalization. The clock-recovered signal shows high quality and stability to be potentially employed as an alternative to electronic circuits in a number of high-speed synchronization applications, i.e., optoelectronic continuous or burst receivers.

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II. WORKING PRINCIPLE

NRZ-DPSK data can be detected using narrow optical filtering [3]. By using this technique, a Gaussian-shaped filter with a full-width at half-maximum (FWHM) of around 60% of the bit rate provides demodulation of the incoming DPSK signal, while also strongly enhancing the signal tolerance to chromatic dispersion [3]. From a physical point of view, the same consecutive symbols (data persistence), which are spectrally close to the carrier, pass through the filter unaffected. On the other hand, data transitions, which are aside in the spectrum, are cut. In [3], this filter was a fiber Bragg grating (FBG) used in reflection together with an optical circulator. We highlight that the part of the input signal transmitted through the FBG is composed by all the transitions in the NRZ-DPSK sequence. Hence, it results in a pulse at every phase transition of the incoming data. Consequently, this sequence contains significant clock components. Using this RZ-like signal, clock recovery can be performed. To this aim, we use the circuit introduced in [4], [5], which is basically made of a Fabry-Pérot filter (FPF) and an SOA. Thanks to the interplay of the memory properties of the high-finesse FPF with the free spectral range (FSR) matching the clock frequency and the power-limiting capability of the saturated SOA, it is possible to fill the zero slots of an incoming RZ data stream with optical pulses. Indeed, even in case of consecutive zero sequences, when the FPF introduces pulses with decaying intensity (and thus amplitude oscillations arise), the saturation effects of the SOA can be used to equalize the pulses amplitude [4]. The effectiveness of this circuit in all-optical regenerative applications was recently demonstrated in an over-million-kilometer transmission experiment with 3R regeneration for both RZ signals [6], and NRZ signals [7].

III. EXPERIMENT AND DISCUSSION

The schematic of the clock extraction circuit is reported in Fig. 1. The optical carrier at $\lambda_1 = 1551.73$ nm is generated by a tunable laser. The NRZ-DPSK signal is obtained by means of a LiNbO₃ phase modulator driven by a $2^7 - 1$ long PRBS sequence at 10.2 Gb/s. The signal is then amplified by an EDFA to 13 dBm to account for the circuit loss. Then it is sent via an optical circulator (OC) to the FBG.

The FBG has a Gaussian reflectivity profile, with FWHM of 6 GHz and an almost flat phase response with low spurious dispersion. Combined with the OC, the FBG provides in reflection effective DPSK demodulation [see Fig. 2(e)] [3]. The optical spectrum of the input DPSK signal and of the transmitted part from the FBG are reported in Fig. 2(a) and (b), respectively. The transmitted part of the signal is composed by a series of pulses, each one produced at a phase transition [Fig. 2(f)]. As can be

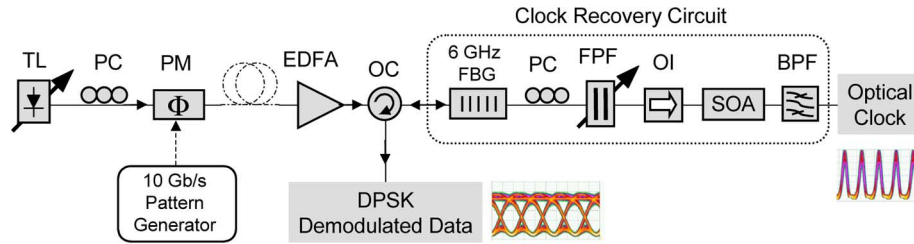


Fig. 1. Experimental setup. TL: tunable laser, PM: phase modulator, EDFA: erbium-doped fiber amplifier, OC: optical circulator, FBG: fiber Bragg grating, PC: polarization controller, FPF: Fabry–Pérot filter, OI: optical isolator, SOA: semiconductor optical amplifier, and BPF: band-pass filter.

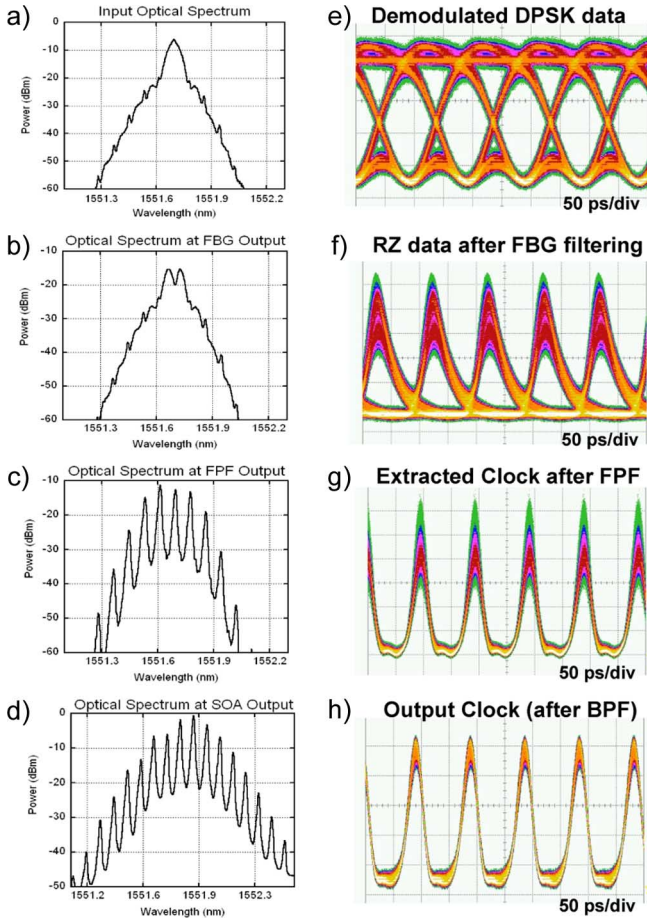


Fig. 2. a)–d) Evolution of the optical spectrum at various points in the clock recovery circuit. Resolution: 0.01 nm. d)–e) Eye diagram of the demodulated input DPSK signal and evolution of the optical clock in the extraction circuit.

seen in Fig. 3(a), the electrical spectrum of this signal has significant clock components.

This RZ-like signal is then sent to the FPF. The FPF is a fiber-based device with FSR = 10.2 GHz, Finesse = 100 (FWHM = 100 MHz). The insertion loss is around 4 dB when working with continuous-wave signals while it increases to around 8 dB when filtering the modulated signal of our experiment.

The filter wavelength can be tuned by means of a temperature controller and a polarization controller is used to maximize the filter output. The FPF selects the spectral clock lines, removing most of the incoming modulation [see Fig. 2(c)], producing a signal composed of 25-ps pulses. However, a significant amount of amplitude oscillations is still present [see Fig. 2(g)]. This is

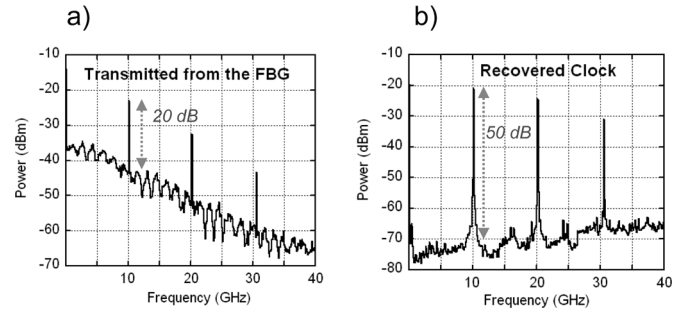


Fig. 3. Electrical spectra at a) the input and b) the output of the clock extraction circuit.

related to the input pattern sequence and it is due to the limited memory effect of the FPF. It is a low-frequency noise that can be strongly reduced by exploiting the high-pass filtering property of the saturated SOA [4], [5]. To this aim, we use a polarization insensitive pigtailed SOA (Alcatel 1901A) with about 28 dB of small signal gain, around 200 ps of recovery time, and 6 dBm of output saturation power at 200-mA driving current. An optical isolator is used to reduce spurious reflections between the FPF and the SOA. The optical power at the SOA input was -2 dBm corresponding to around 20 dB gain compression. Finally, a 0.8-nm band-pass filter selects part of the optical spectrum at SOA output and removes the out-of-band ASE. The amplitude noise reduction effect can be clearly observed in the oscilloscope trace of Fig. 2(h), while the effect of the SOA on the optical spectrum is reported in Fig. 2(d). Here, we see some spectral red-shift due to pulse self phase modulation in the SOA. This effect combined with a slight detuning of the output BPF helps in amplitude noise reduction and ER improvement [8]. At the same time, in the electrical spectrum [reported in Fig. 3(b)] most of the modulation components presented by the RZ-like signal of Fig. 3(a) are removed.

In the corresponding electrical spectrum, the carrier-to-modulation ratio for the recovered clock increases to more than 50 dB. Finally, at the output of the clock extraction circuit, we obtain a sequence of 25-ps pulses at the input data rate. Their root-mean-square (rms) time jitter was measured by a sampling oscilloscope using the precision time-base reference module (having rms time jitter resolution <200 fs). It resulted in around 710 fs [Fig. 4(a)] for input OSNR ranging from 20 to 40 dB [Fig. 4(c)]. This value is reduced by more than four times in respect to the one of the input DPSK signal (about 2.9 ps) reported for comparison in Fig. 4(b). We note that in this last case this value includes a certain amount of inter-symbol

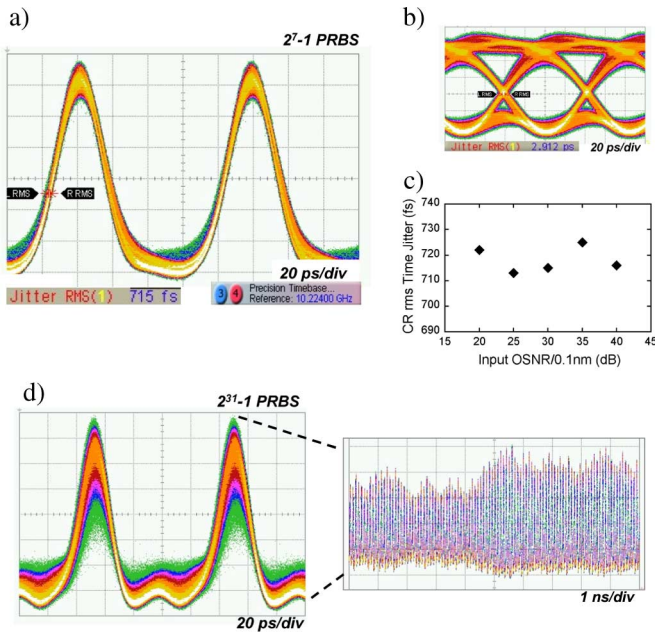


Fig. 4. a) Time jitter measurement on the recovered clock. b) Time jitter measurement on the data. c) Recovered clock time jitter versus input OSNR. d) Persistence trace and detail of the output clock sequence in case of $2^{31} - 1$ sequence.

jitter due to the demodulation process that cannot be exactly evaluated. Nevertheless, input-output jitter reduction can be expected by previous results [9].

The present results, obtained for a $2^7 - 1$ long PRBS, were limited by the Finesse of the available FPF. Indeed, when using longer sequences, as shown in Fig. 4(d), residual pattern-related amplitude oscillation leads to a degradation of the clock recovered signal. For such sequences, a filter with higher finesse is required. From our previous work [5], we infer that a filter with a finesse of around 250 can guarantee efficient operation up to a $2^{31} - 1$ long sequence.

Finally, the good quality and stability of this clock is confirmed by two additional measurements. We first triggered with the recovered clock a sampling oscilloscope to measure the eye diagram. After 1-h accumulation time, we find the eye diagram still completely fixed and open (see inset of Fig. 5). Supporting this observation, Fig. 5 reports a comparison of the bit-error-rate (BER) curves obtained using as clock signals, the electrical clock from the pattern generator, an electrical clock recovered with an electrical clock recovery unit, and the optical clock recovered signal by our scheme, respectively. No appreciable power penalty can be observed using the various synchronization signals.

IV. CONCLUSION

We demonstrated clock recovery from NRZ-DPSK data for the first time. For a 10-Gb/s signal, we obtained a 10-GHz clock

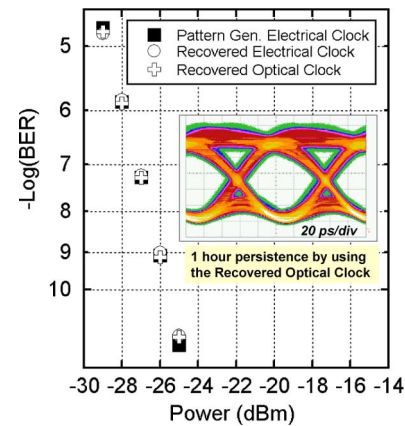


Fig. 5. BER comparison using different clock signals. In the inset, demodulated DPSK eye diagram with 1-h persistence time obtained when triggered by the optical clock.

of 25-ps pulses having 710-fs rms time jitter. The recovered clock signal has comparable performance with the original electrical synchronization signal in both oscilloscope triggering and BER measurements. Due to its working principle, the present technique can be extended to higher bit rates by scaling by the same factor the FBG-FWHM and the FPF-FSR. When increasing the bit rate, wavelength stability requirements of the scheme are relaxed.

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